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SC Cropps, TR Allen, ... - US Patent 5,204,613, 1993 - Google Patents
 ... bypass capacitor 140. ... 122e con-nected in series with a damping resistance 152 ... discrete packaged devices 50 interconnected on a printed circuit board, chip and ...

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K Kundert - The Designer's Guide, 2004 - designers-guide.com

... part of the total damping resistance. ... tying the two supplies together with a bypass capacitor, as in ... A little effort taken during circuit board layout can save ...

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A monolithic 156 Mb/s clock and data recovery PLL circuit using thesample-and-hold technique - all 5 versions »

N Ishihara, Y Akazawa - Solid-State Circuits, IEEE Journal of, 1994 - IEEE Xplore IEEE.org
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N Mutoh, M Kanesaki, J Nakashima, M Ogata - Industry Applications Conference, 2003. 38th IAS Annual ..., 2003 - IEEE Xplore IEEE.org

... a method is proposed that controls both the micro-surge occurring at motor terminals and the shaft current using multi-layer power printed circuit technique. ...

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Package technology for STM-16 optical Tx/Rx and IC's

A Takai, C Kamada, Y Hattai, S Aoki, A Murata, K ... - Electronic Components and Technology Conference, 1991. ..., 1991 - IEEE Xplore IEEE.org

... our driver IC chip, only a 100 damping resistance can be ... with an IC and two termination/bypass capacitor chips. ... on the GND plate of the circuit board with so ...

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Tuner circuit having a switching diode to change receiving bands - all 3 versions »

M Kitaguchi - US Patent 5,463,362, 1995 - freepatentsonline.com

... and the band switching terminal, functions as a damping resistance for the tuning circuit, there is ... to reduce the packaging density on the board having a ...

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[BOOK] Electronic System Design: Interference and Noise Control Techniques

JR Barnes - 1967 - Prentice Hall

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Circuit board device and design support device - all 3 versions »

O Ueno, H Arakaki - US Patent 7,068,518, 2006 - Google Patents

... Filtering and damping resistance have come to be used ... a method includes providing

a bypass capacitor in the ... power supply layer 112 of a printed circuit board ...

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[A method to control EMI noises generated by micro-surges appearing at terminals of electric machines ... - all 2 versions »](#)

N Mutoh, M Kanesaki, J Nakashima - Applied Power Electronics Conference and Exposition, 2004. 2004 - [ieeexplore.ieee.org](#)

... board is composed of a three-layer printed power circuit ... Parameters Used in the Proposed EMI Control Board First, the ... b) and (c) show that the circuit of the ...

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[CITATION] [Currents Focusing on Common Mode Current Paths Produced in Motor Drive Systems](#)

N Mutoh, M Kanesaki, J Nakashima, M Ogata - Conference Record of the 2003 IEEE Industry Applications ..., 2003 - Institute of Electrical & Electronics Engineers (IEEE)

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